	Туре	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1		(phase change memory) and @ad<"20040220"	US- PGPUB; USPAT; EPO; DERWE NT; IBM_T DB	2005/05/0 3 07:27
2	BRS	L2	124.1.1	1 and (mosfet or fet) and gate	US- PGPUB; USPAT; EPO; DERWE NT; IBM_T DB	2005/05/0 3 07:28